

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re Application of

Atty. Docket

THOMAS RICHTER

DE 000052

Serial No.

Group Art Unit

Filed: CONCURRENTLY

Examiner:

Title: NETWORK ELEMENT OF AN ANALOG, CELLULAR NETWORK, AND
METHOD FOR SUCH A NETWORK ELEMENT

Commissioner for Patents
Washington, D.C. 20231

PRELIMINARY AMENDMENT

Sir:

Prior to calculation of the filing fee and examination,
please amend the above-identified application as follows,
where marked-up versions of the amended claims 3, 4 and 5
are attached as Appendix A:

IN THE CLAIMS

Please amend the claim as follows:

1 3. (Amended) A network element as claimed in claim 1,
2 characterized in that the evaluation means are arranged to
3 select for further processing that repeat from the received
4 repeats of a data word (REP1-REP11) in a data sequence that
5 occurs most frequently.

1 4. (Amended) A network element as claimed in claim 1,
2 characterized in that the evaluation means include a memory
3 for storing a correct starting synchronization (DOT1) and a
4 data buffer which has a capacity at least equal to the
5 starting synchronization for the bit-wise storage and
6 shifting through of the received data, as well as

7 comparison means for the continuous bit-wise comparison of
8 the stored memory contents with the data buffer contents
9 and for determining the number (dd(rx)) of deviating bits,
10 the evaluation means being arranged to decide that a
11 starting synchronization (DOT1) has commenced when the
12 number (dd(rx)) of deviating bits is less than a
13 predetermined number (dd_{min}), and that a starting
14 synchronization (DOT1) has been correctly received when the
15 number (dd(rx)) of deviating bits reaches zero.

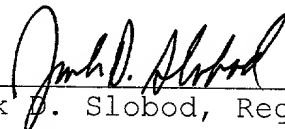
1 5. (Amended) A network element as claimed in claim 1,
2 characterized in that the evaluation means are arranged to
3 assume the occurrence of a change over to the second data
4 sequence in the case of disturbed starting synchronizations
5 (DOT1) of two directly successive data sequences after
6 expiration of the temporal length of a data sequence as
7 from the beginning of a first recognized synchronization
8 (DOT) that is succeeded by a correct word synchronization
9 (WS).

REMARKS

The claims have been amended to delete multiple dependencies.

The above amendments are submitted to place this application in proper U.S. format. Entry of the amendment and an early action on the merits are solicited.

Respectfully submitted,

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Appendix A

Version with Markings to Show Changes Made to the Claims

The following are marked up versions of amended claim

3, 4 and 5:

1 3. (Amended) A network element as claimed in claim 1 ~~one~~
2 ~~of the preceding claims~~, characterized in that the
3 evaluation means are arranged to select for further
4 processing that repeat from the received repeats of a data
5 word (REP1-REP11) in a data sequence that occurs most
6 frequently.

1 4. (Amended) A network element as claimed in claim 1 ~~one~~
2 ~~of the preceding claims~~, characterized in that the
3 evaluation means include a memory for storing a correct
4 starting synchronization (DOT1) and a data buffer which has
5 a capacity at least equal to the starting synchronization
6 for the bit-wise storage and shifting through of the
7 received data, as well as comparison means for the
8 continuous bit-wise comparison of the stored memory
9 contents with the data buffer contents and for determining
10 the number (dd(rx)) of deviating bits, the evaluation means
11 being arranged to decide that a starting synchronization
12 (DOT1) has commenced when the number (dd(rx)) of deviating
13 bits is less than a predetermined number (dd_{min}), and that a
14 starting synchronization (DOT1) has been correctly received
15 when the number (dd(rx)) of deviating bits reaches zero.

1 5. (Amended) A network element as claimed in claim 1 ~~one~~
2 ~~of the preceding claims~~, characterized in that the

3 evaluation means are arranged to assume the occurrence of a
4 change over to the second data sequence in the case of
5 disturbed starting synchronizations (DOT1) of two directly
6 successive data sequences after expiration of the temporal
7 length of a data sequence as from the beginning of a first
8 recognized synchronization (DOT) that is succeeded by a
9 correct word synchronization (WS).